dc-SQUID With Input Coil









Lock-in operation





Filux modulation by about $\frac{1}{2} \Phi_0$

WS 22/23

- for quasi static flux between $n\Phi_0$ and $(n+1/4) \Phi_0$ output voltage inreases linearly
- phase sensitive detection possible

Two-stage SQUID configuration

WS 22/23







Two-Stage configuration with series SQUID array











Reverse Current Biasing





۰Vf

Bias

Clock

 R_{f}

Integrator

Reverse Current Biasing



